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## U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10082205	FILING DATE 02/26/2002	CLASS 257	SUBCLASS 314	GAU 2811	EXAMINER T-TRAW
<b>**APPLICANTS:</b> Osabe Taro; Ishii Tomoyuki;					
<b>**CONTINUING DATA VERIFIED:</b>					
NONE TT					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-253887 08/24/2001 YES TT					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		520.41287X00			
Verified and Acknowledged Examiners's initials TT					
TITLE : Semiconductor memory element, semiconductor device and control method thereof					
U.S. DEPT. OF COMM./PAT. & TM-PTO-438L (Rev. 12-94)					

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>		
			Total Claims	Print Claim for O.G.	
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>		
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>		
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